FIG. 1A

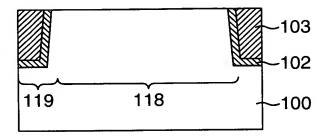


FIG. 1B

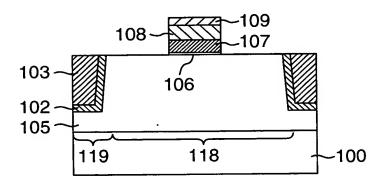


FIG. 1C

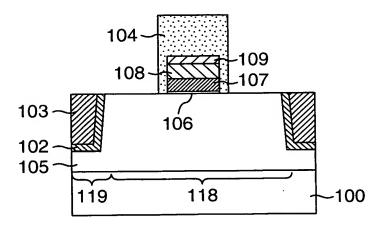


FIG. 1D

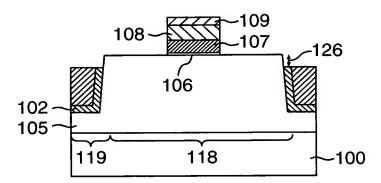


FIG. 1E

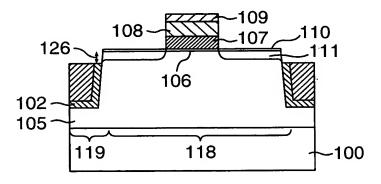


FIG. 1F

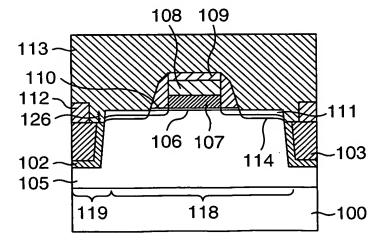


FIG. 1G

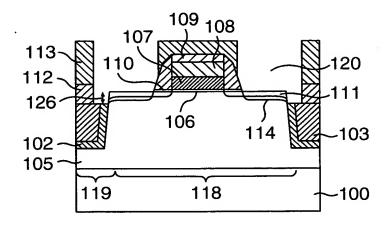


FIG. 1H

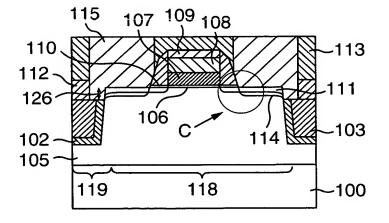


FIG. 2

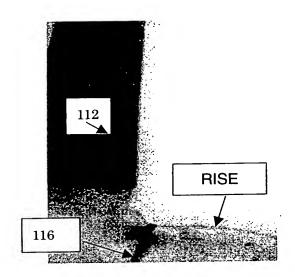


FIG. 3

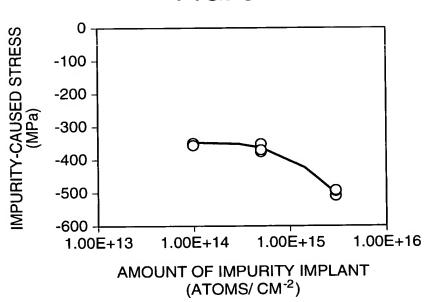


FIG. 4

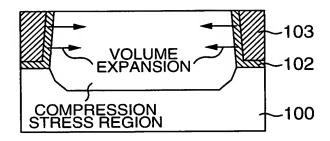


FIG. 5

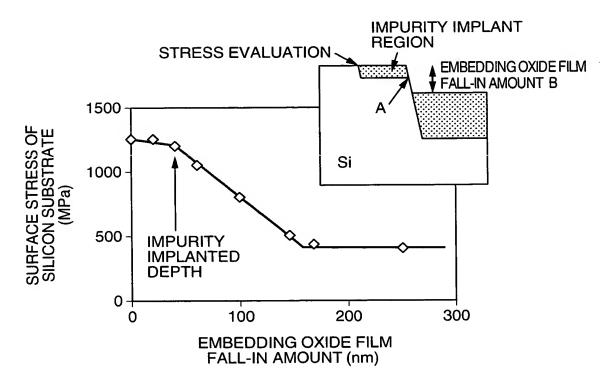


FIG. 6

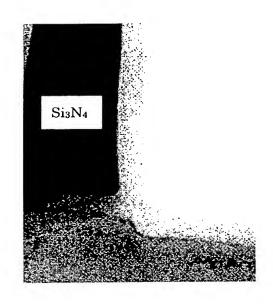


FIG. 7

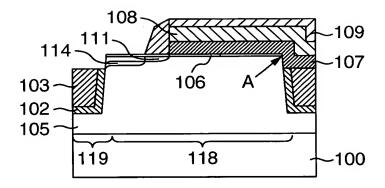
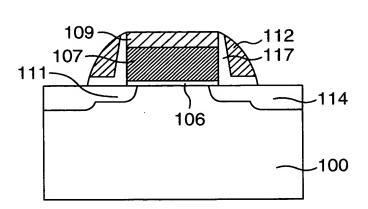


FIG. 8



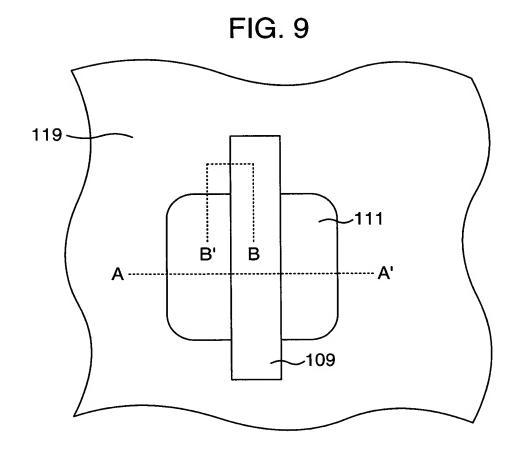
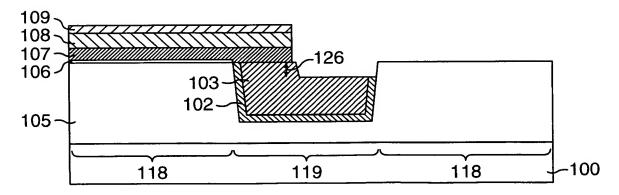
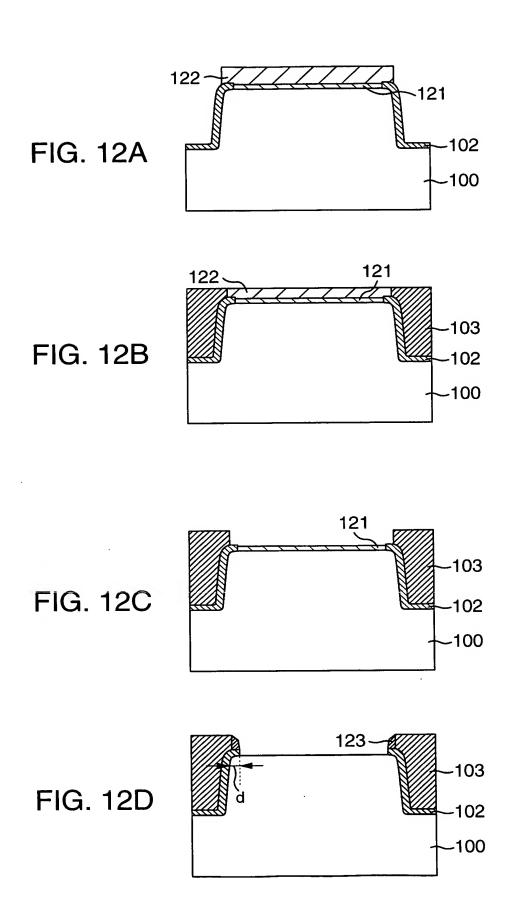


FIG. 10



-121 FIG. 11A -100 -123 121 FIG. 11B -100 -103 121 122 FIG. 11C -102 -100 -103 FIG. 11D -102 -100



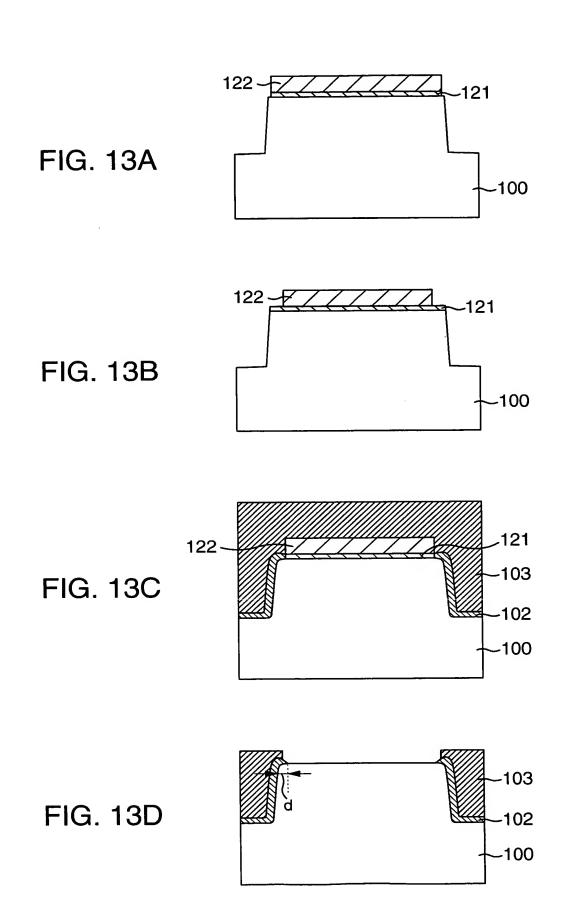


FIG. 14A

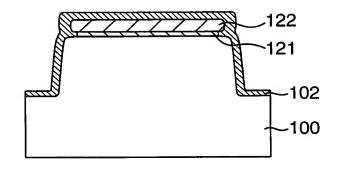


FIG. 14B

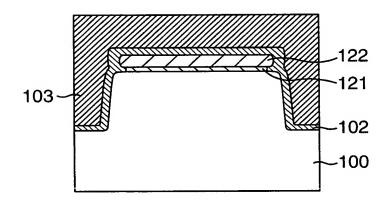
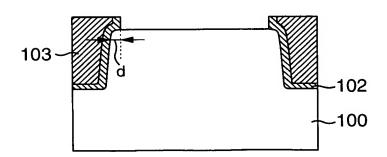


FIG. 14C



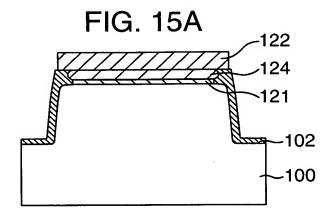


FIG. 15B

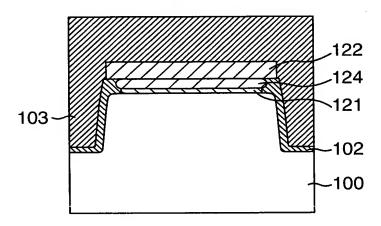


FIG. 15C

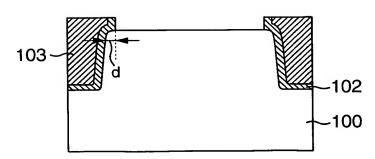


FIG. 16A

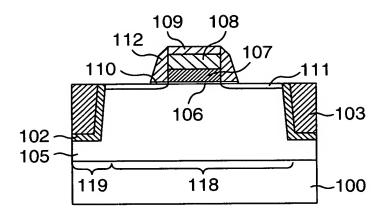


FIG. 16B

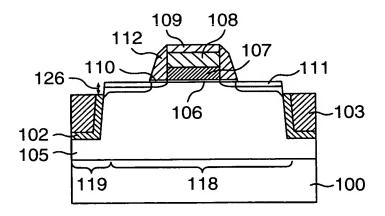


FIG. 17

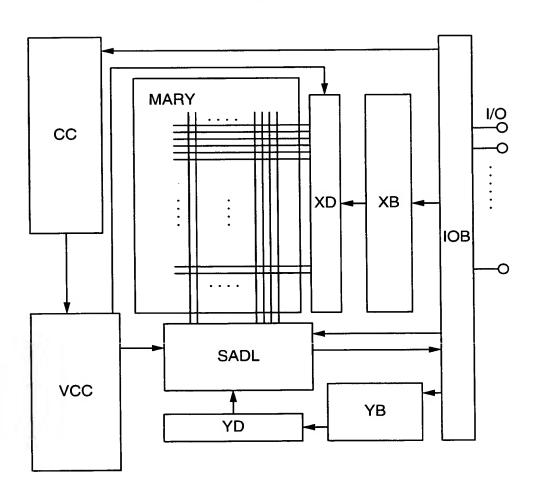


FIG. 18

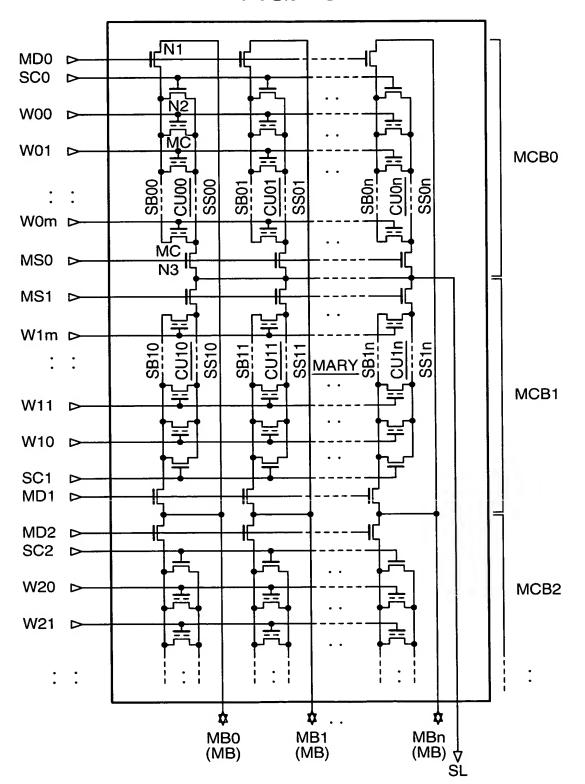


FIG. 19

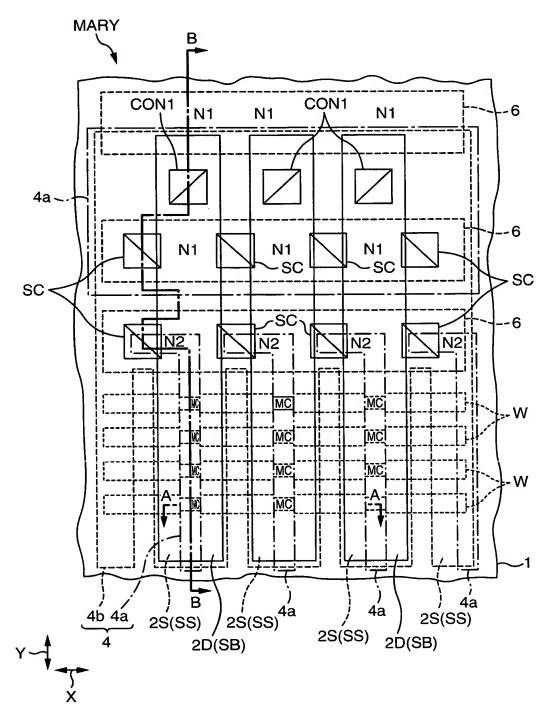


FIG. 20

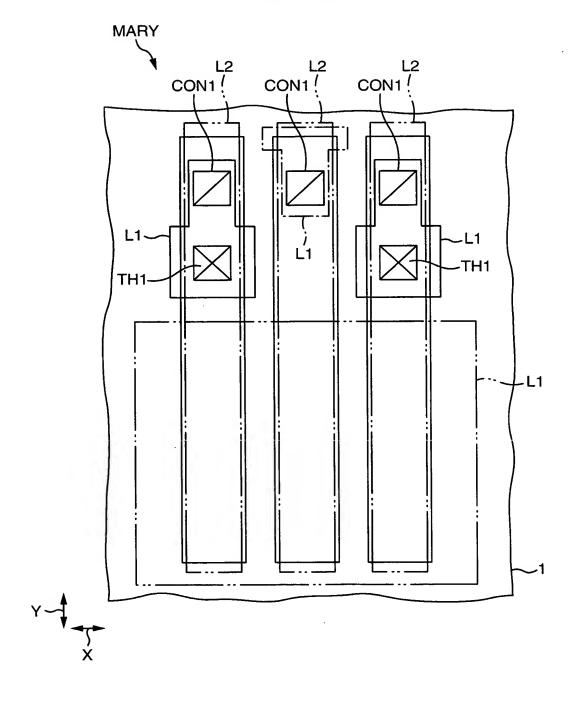


FIG. 21A

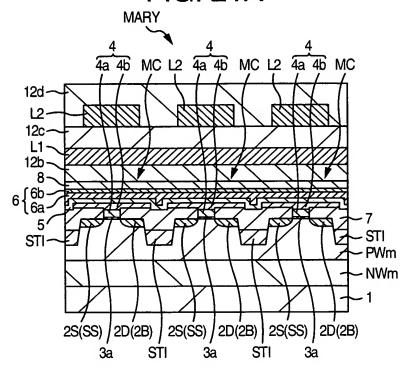


FIG. 21B

STI: ELEMENT ISOLATING PORTION

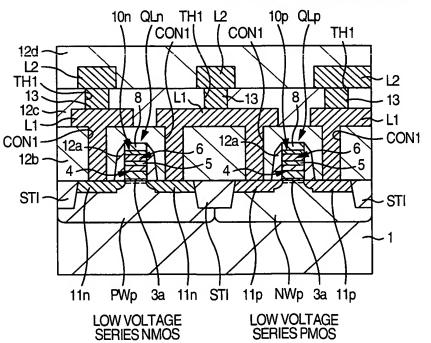


FIG. 22

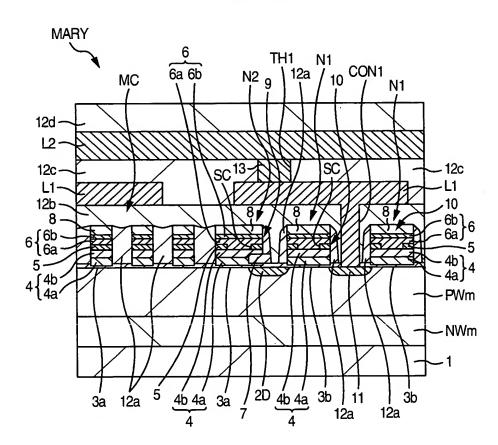
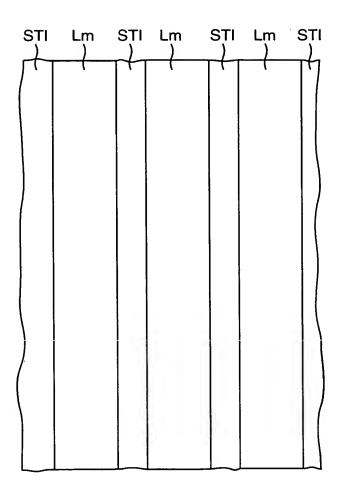
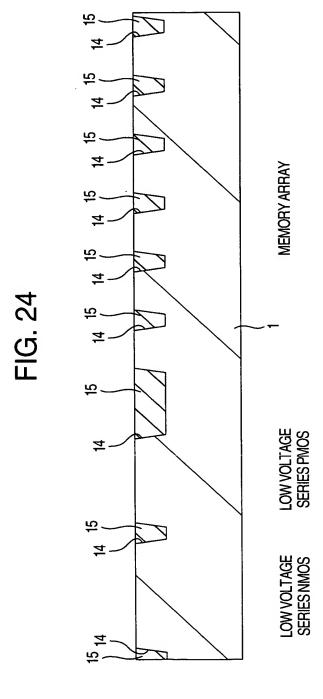


FIG. 23





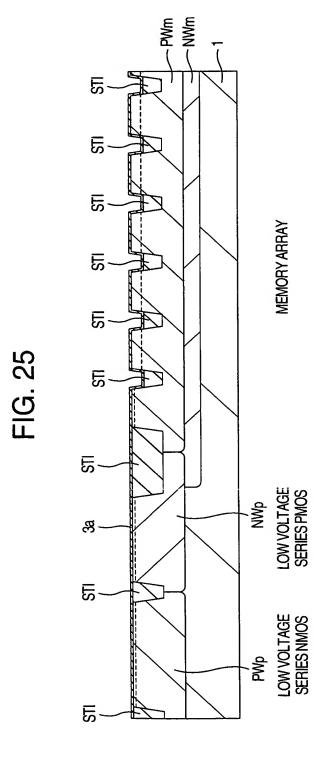
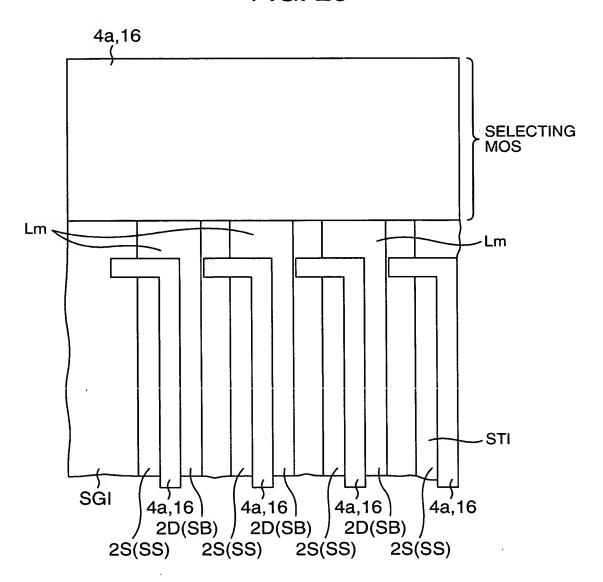


FIG. 26



25 / 2b 3a 3a 3a 3a 3a 3a MEMORY ARRAY FIG. 27 S LOW VOLTAGE SERIES PMOS NWp 16 STI 4a 3a LOW VOLTAGE SERIES NMOS ജ. PWp E.

-PWm -NWm

; / z'b z's / z'b z's / z'b z's / z'b 3a 3a 3a 3a 3a S \$ STI MEMORY ARRAY \$ S 9 S 9 25 / 20 25 / 3a 3a STI 9 <u>\$</u> STI LOW VOLTAGE SERIES PMOS NWp 9 æ STI LOW VOLTAGE SERIES NMOS 4 ଞ୍ଚ PWp

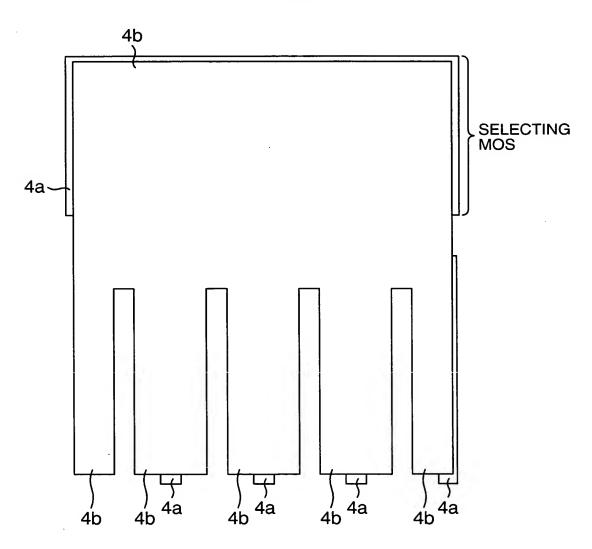
ST

- PWm - NWm

9

FIG. 28

FIG. 29



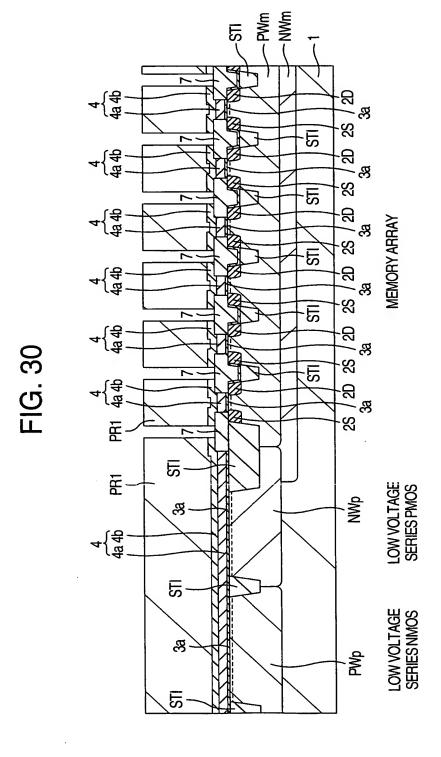
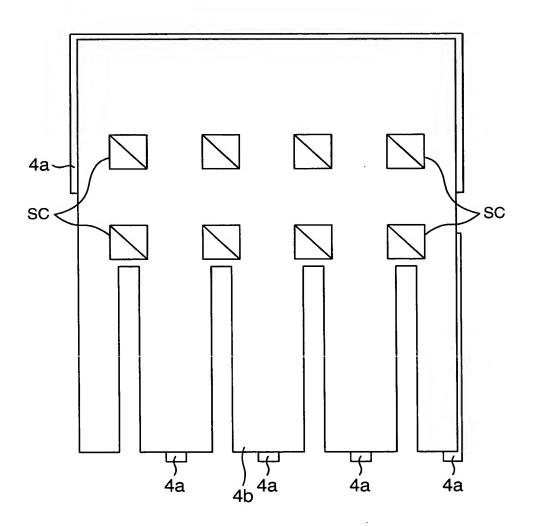


FIG. 31



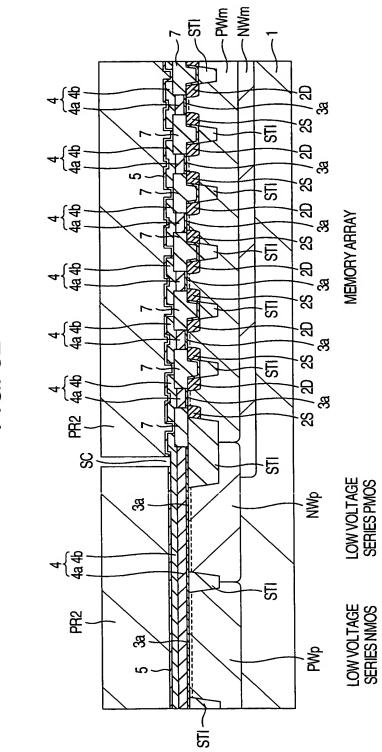
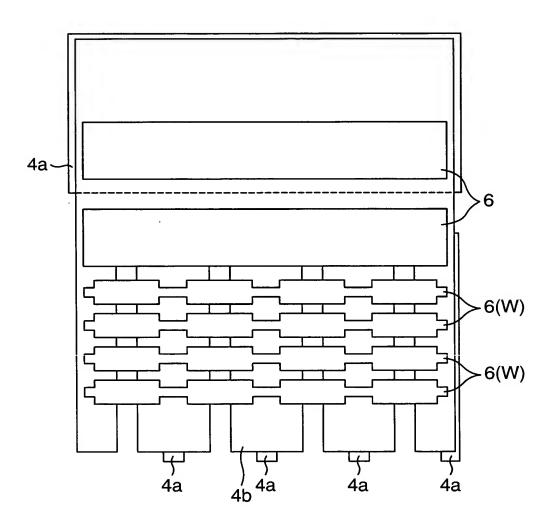


FIG. 32

FIG. 33



- PWm LST. 4{a 4 25 / 2b 3a 3a 3a 3a 3a 3a 3a ST 48 4 4 S **MEMORY ARRAY** 4 {합 STI 4 4 4 S PR3 600) $\stackrel{\circ}{\aleph}$ S LOW VOLTAGE SERIES PMOS 3a NWp 4a 各 SŢ 8 6 6 6 6 6 LOW VOLTAGE SERIES NMOS PWp 3a H 유 STI

FIG. 34

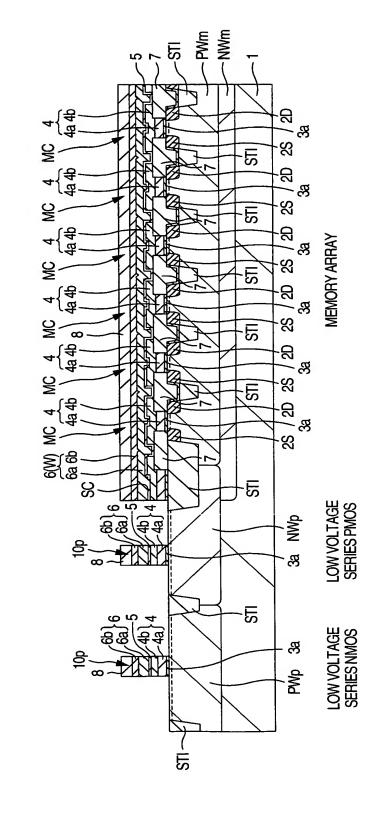


FIG. 35

-PWm _NWm 2ś / 2b 3a 3a 3a 3a 3a 3a 区 ST MEMORY ARRAY ST S 12a SC S NWp LOW VOLTAGE SERIES PMOS 11pa 88 STI 11pg LOW VOLTAGE SERIES NMOS 12a PWp 3a

FIG. 36

FIG. 37

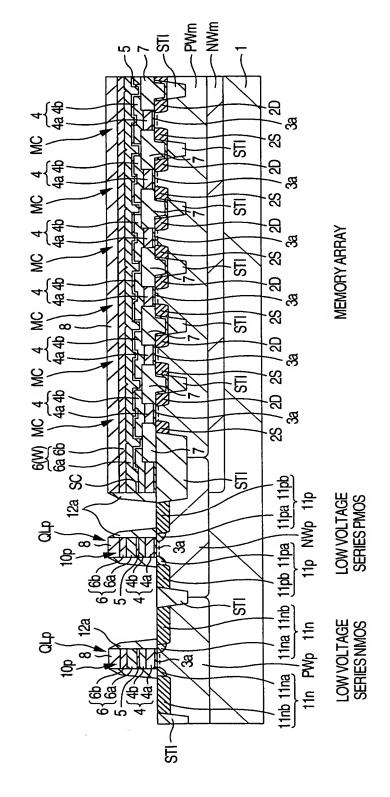


FIG. 38

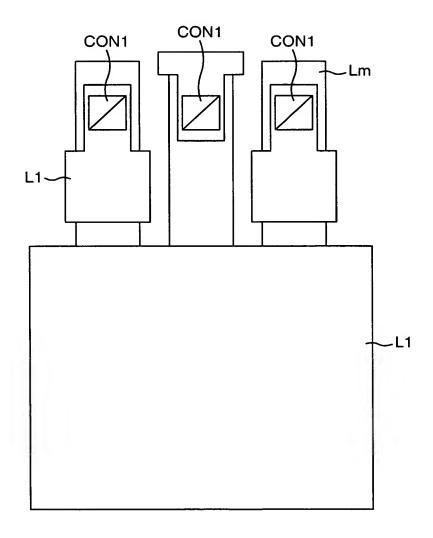


FIG. 39

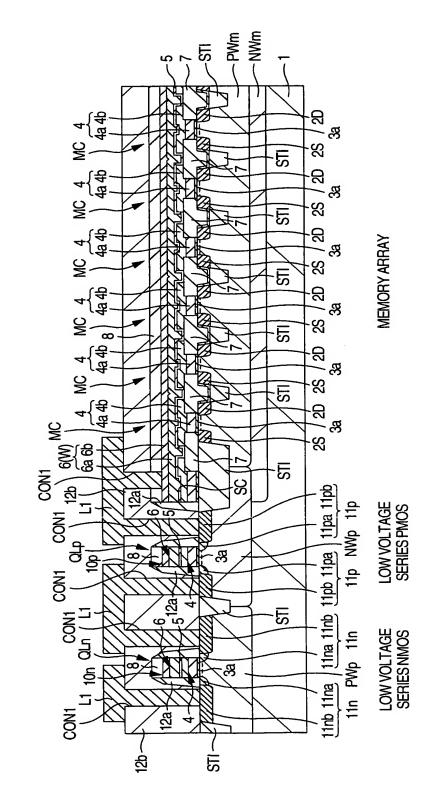
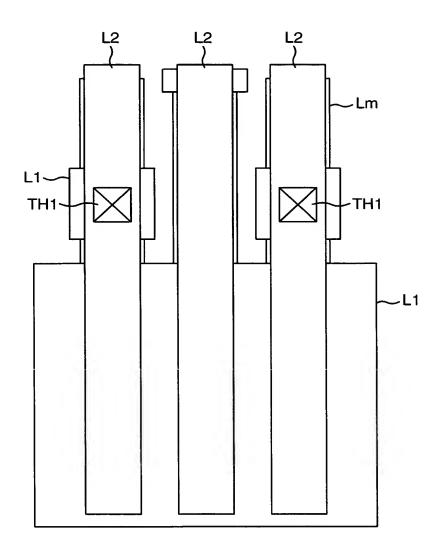


FIG. 40



-PWm MWM (-STI /2D %a 3/20/25 38 ST 2s / 2b 2s / 2b 2s / 2b 2s / 2b 2s 3a 3a 3a 3a 3 S **MEMORY ARRAY** MC 4a 4b 4a 4b 8 S 8 ST 4a 4a 4b 8 B ST 11nb 11na | 11na 11nb 11pa | 11pa 11pb LOW VOLTAGE SERIES PMOS 11p NWp 11p 4 9 9 R E NOS LOW VOLTAGE SERIES NMOS / ray of / 11n PWp 11n <u>8</u> STI € 7 SONT

